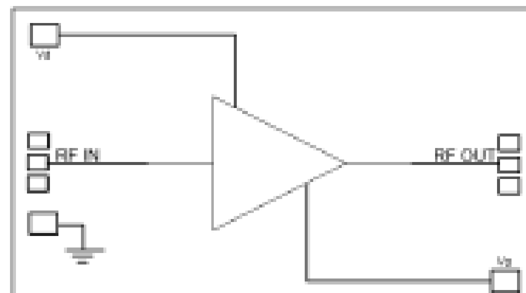
Features

- Frequency: DC-20GHz
- Small Signal Gain: 13dB
- Noise Figure: 2.0dB typ./4.5dB max.
- P1dB: 16dBm
- Psat: 18dBm
- Power supply: +8V/60mA
- Input/Output: 50Ω
- Die Size: 2.5 x 1.6 x 0.1 mm

Functional Block Diagram



Typical Applications

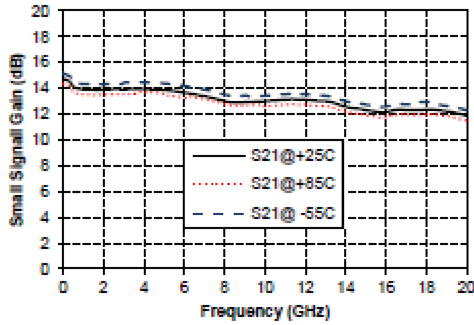
- Test Instrumentation
- Microwave Radio & VSAT
- Military & Space
- Telecom Infrastructure
- Fiber Optics

Electrical Specifications

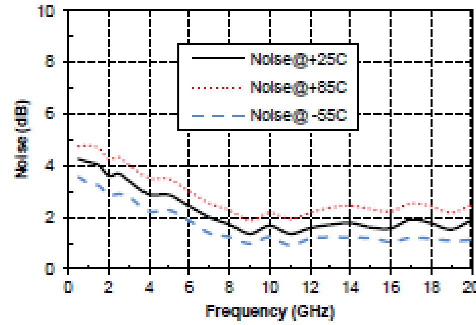
TA = +25ºC, Vd = +8V

Parameters	Min.	Typ.	Max.	Units
Frequency		DC-20		GHz
Small Signal Gain	11.5	13	14.5	dB
Gain Flatness		±1.5		dB
Noise Figure	1.5	2.0	4.5	dB
Output 1dB Compression (P1dB)	14	16	16.5	dBm
Saturated Output Power (Psat)	14.5	18	18.5	dBm
Input Return Loss		19		dB
Output Return Loss		19		dB
Static Current		60		mA

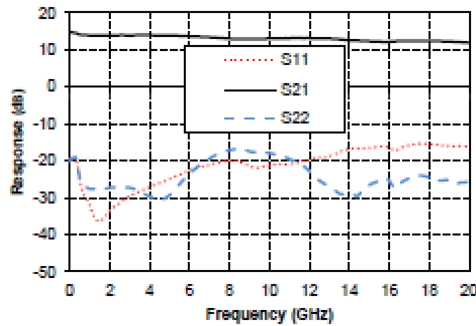
Gain vs. Frequency



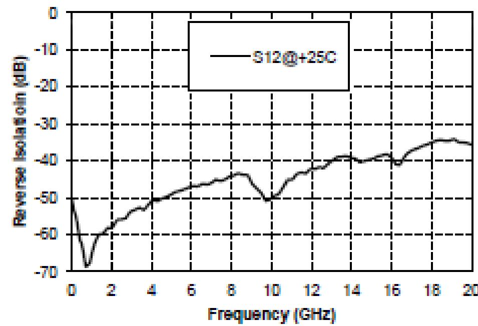
Noise Figure vs. Frequency



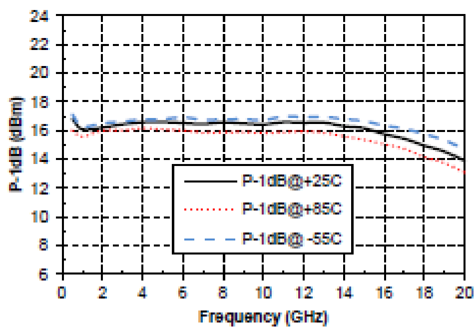
Gain&Return Loss vs. Frequency



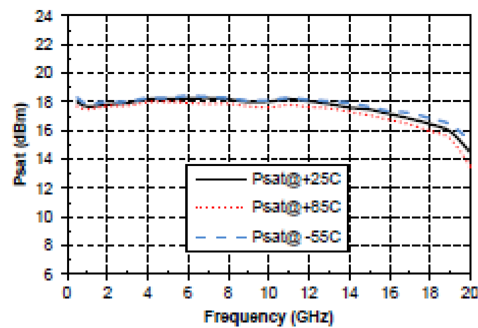
Reverse Isolation vs. Frequency



P1dB vs. Frequency

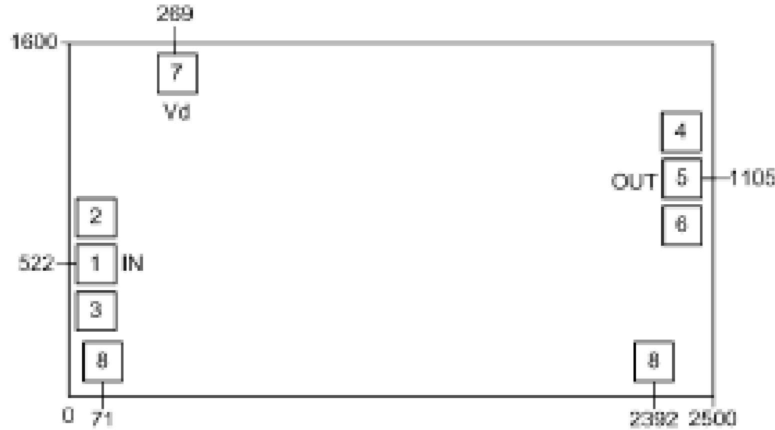


Psat vs. Frequency



Outline Drawing:

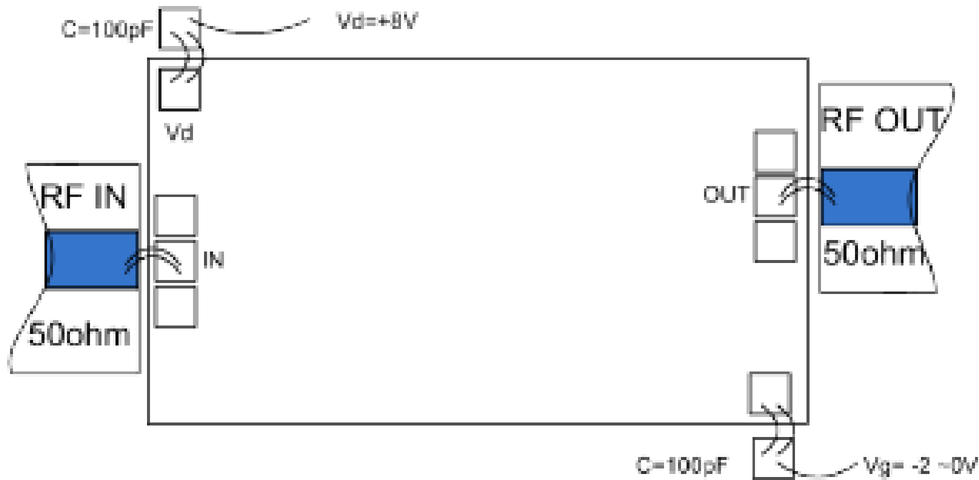
All Dimensions in µm



Pad Description

Pad	Function	Description	Equivalent Circuit
1	RF IN	RF signal input terminal, blocking capacitor required.	RF IN
7	VDD	Amplifier drain bias; external 100pF bypass capacitor required.	
5	RF OUT	RF signal output terminal, blocking capacitor required.	RF Out
8	VG	Amplifier gate bias; external 100pF bypass capacitor required.	Vg
2, 3, 4, 6, 9, die bottom	GND	Die bottom must be connected to RF/DC ground.	GND

Assembly Drawing



Notes:

1. Die thickness: 100um
2. Typical bond pad is 100*100 μm^2
3. Bond pad metalization: Gold
4. Backside metalization: Gold
5. Backside of the die (GND)
6. No connection required for unlabeled bond pads

Maximum Ratings:

1. Maximum drain voltage: +12V
2. Maximum input power: +18dBm
3. Operating temperature: -55℃ to +85℃
4. Storage temperature: -65℃ to +150℃