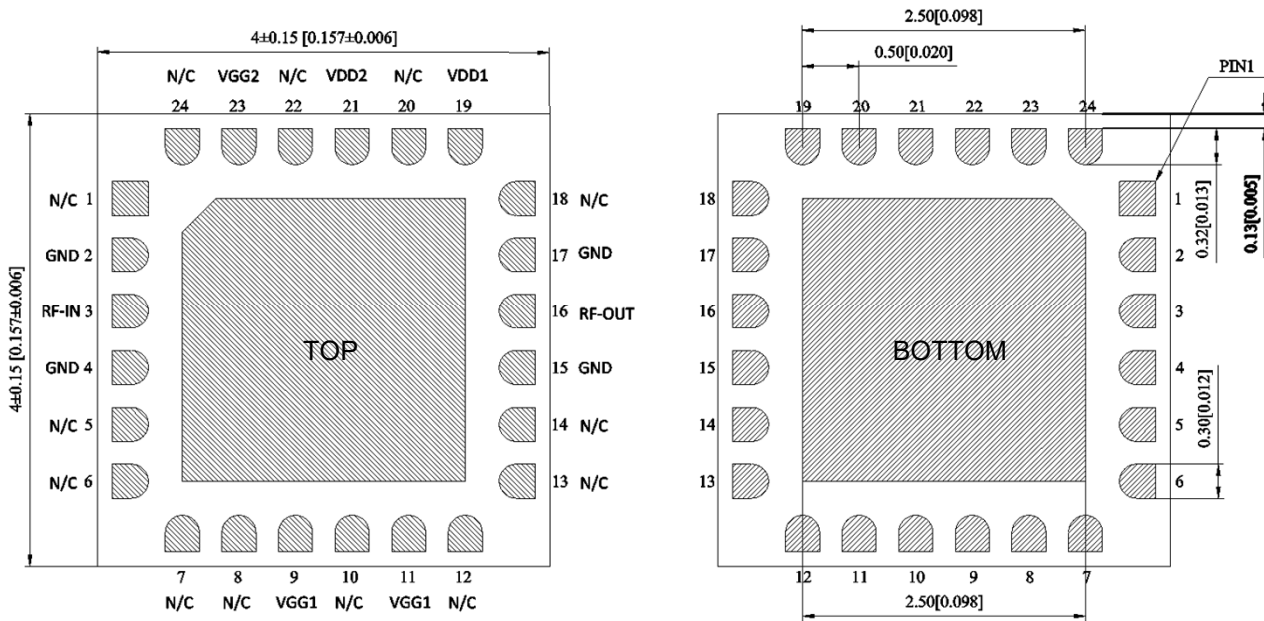


Dimensions and pin description:



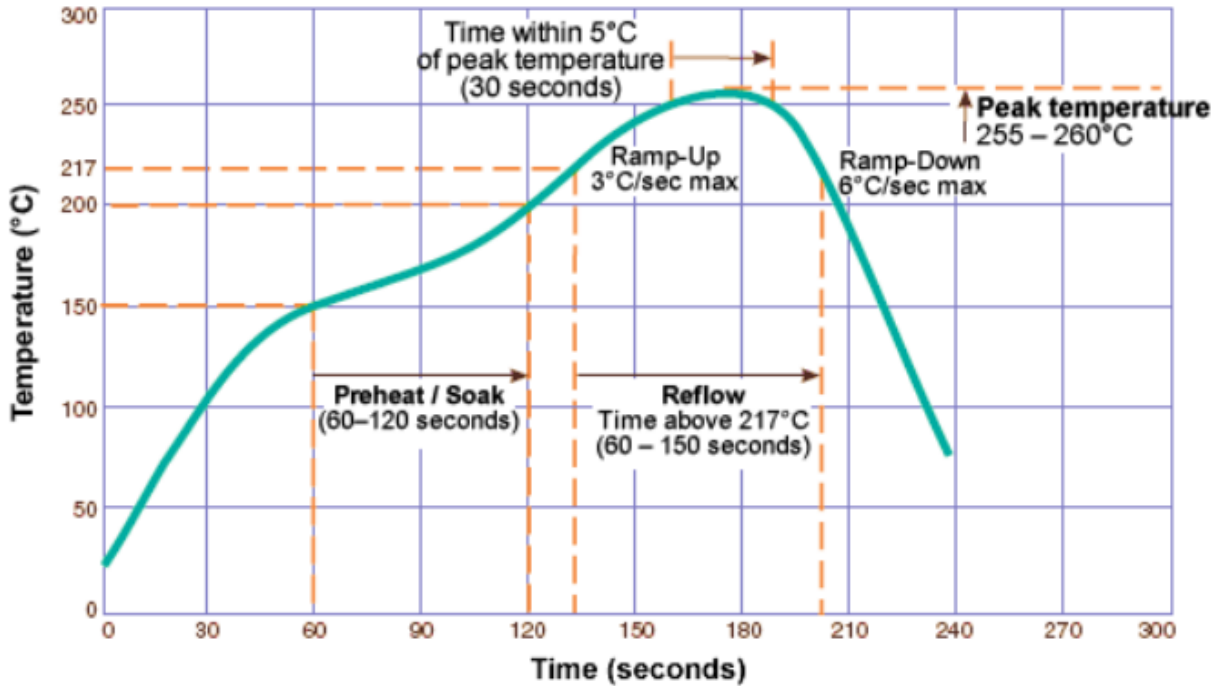
Notes:

1. Pad plating Ni: 1.27-8.89um Au: 0.3um min
2. Dimension shown in mm [inch] Tolerances $\pm 0.05\text{mm}$ [$\pm 0.002\text{inch}$]

| Pin Descriptions | | |
|------------------|-----------|--|
| Pin Number | Function | Description |
| 3 | RF-IN | This pad is DC coupled and matched to 50 Ohms |
| 16 | RF-OUT | RF output for amplifier |
| 19,21 | VDD2,VDD1 | Connect DC bias (Vdd) network to provide drain current (Idd) |
| 9,11 | VGG1 | Gate control for amplifier |
| 23 | VGG2 | Gate control for amplifier |
| 2,4,15,17 | GND | These pins & exposed ground paddle must be connected to RF/DC ground |
| Die Bottom | GND | Die bottom must be connected to RF/DC ground |



Recommended lead-free reflow temperature curve:



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