

GaAs QFN 4x4mm Low Noise Amplifier 6-14GHz

Features

Single Biasing Voltage (Self Biased)

• Frequency: 6-14GHz

Small Signal Gain: 23dBTypical
Gain Flatness: ±0.25dB Typical
Noise Figure:0.8dB Typical

• P1dB: 16dBm Typical

• Power Supply: +5V@52mA

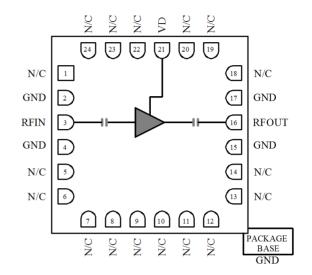
• Input/Output: 50Ω

• Package Size: 4 x 4 x 0.87mm

Typical Applications

- Test Instrumentation
- Microwave Radio & VSAT
- Military & Space
- Telecom Infrastructure
- Fiber Optics

Functional Block Diagram



Electrical Specifications

TA = +25°C, VD = +5V, IDD = 52mA Typical

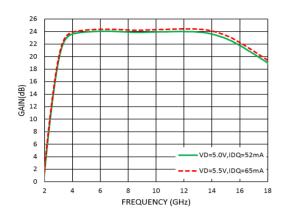
| Parameters | Min. | Тур. | Max. | Units |
|-------------------------------------|------|-------|------|-------|
| Frequency | 6 | | 14 | GHz |
| Small Signal Gain | 22 | 23 | | dB |
| Gain Flatness | | ±0.25 | | dB |
| Noise Figure | | 0.8 | 1.4 | dB |
| P1dB - Output 1dB Compression | 13 | 15 | | dBm |
| Psat - Saturated Output Power | | 16.5 | | dBm |
| OIP3 - Output Third Order Intercept | | 26 | | dBm |
| Input Return Loss | | -14 | | dB |
| Output Return Loss | | -12 | | dB |

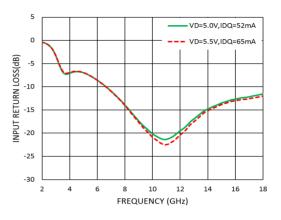


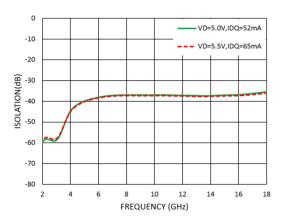
V2.0.

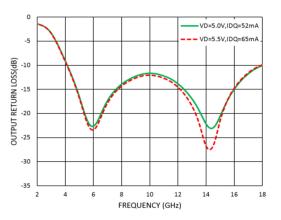
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Measurement Plots: S-parameters

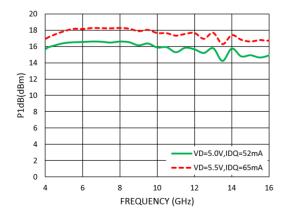




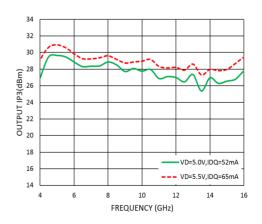




Measurement Plots: P1dB



Measurement Plots: OIP3



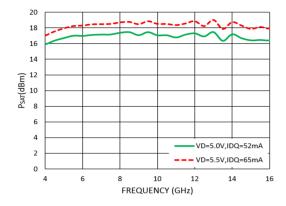
Sales: sales@millermmic.com



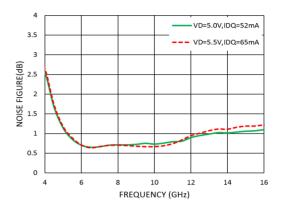
V2.0.0

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Measurement Plots: PSAT



Measurement Plots: Noise Figure



Absolute Maximum Ratings

| Drain Bias Voltage (VD) | +7V | |
|---|------------------|--|
| RF Input Power (RFIN)@(+5V) | +20dBm | |
| Channel Temperature | 175°C | |
| Continuous Pdiss (T = 85 °C) (derate 7.2mW/°C above 85 °C) | 0.65W | |
| Thermal Resistance (channel to die bottom) | 50°C/W | |
| Operating Temperature | -40°C to +85 °C | |
| Storage Temperature | -65°C to +150 °C | |

Typical Supply Current vs. VD

| VD (V) | IDD (mA) | |
|--------|----------|--|
| +5.0 | 52 | |
| +5.5 | 65 | |



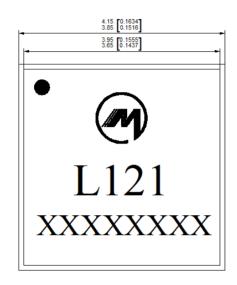
ELECTROSTATIC SENSITIVE DEVICE OBSERVE HANDLING PRECAUTIONS

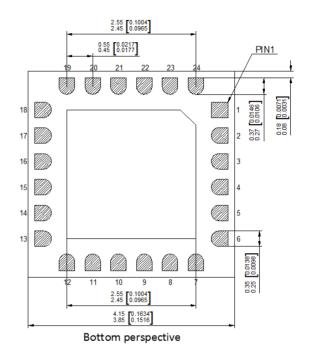


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Outline Drawing:

All Dimensions in mm[inches]







Notes:

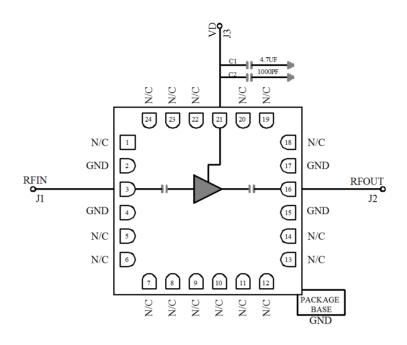
- 1. Package body material : Alumina.
- 2. Lead and ground paddle plating: Gold flash over nickel.
- 3. Dimensions are in millimeters(inches).
- 4. Lead spacing tolerance is non-cumulative.



V2.0.

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Assembly Drawing



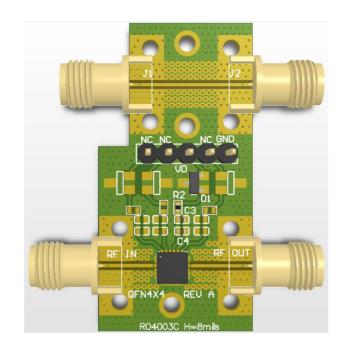
Pin Descriptions

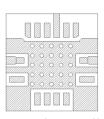
| No | Function | Description |
|--|----------|--|
| 1,5,6,7,8,9,10,11,12,13, 14,18,19,20,22,23,24 | NC | No connection. These pins may be connected to RF ground. Performance will not be affected. |
| 3 | RF IN | RF Signal Input. This pad is ac-coupled and matched to 50 Ω . |
| 16 | RF OUT | RF Signal Output. This pad is ac-coupled and matched to 50 Ω . |
| 21 | VD | Connect to external 1000pf and 4.7uf bypass capacitors. |
| 2,4,15,17 | GND | These pins & exposed ground paddle must be connected to RF/DC ground |
| 25 | GND | Package bottom must be connected to RF/DC ground |



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Evaluation Board Layout Assembly and Mounting Pattern





Mounting Detail

Top dielectric material is ROGERS 4003C, 0.008 inch thickness with 0.5 oz copper.

The pad pattern shown above has been developed and tested for optimized assembly at Miller. The PCB land pattern has been developed to accommodate lead and package tolerances. Since surface mount processes vary from company to company, careful process development is recommended.

Ground / thermal vias are critical for the proper performance of this device. Vias should use a 0.008~0.01 in. diameter drill, filled with copper plating.

Bill of Materials

| Reference Des. | Value | Description | Manuf. |
|----------------|--------|-----------------------------|---------|
| С3 | 4.7uF | Cap, 0603, +10V, ±10 %, X5R | Various |
| C4 | 100pF | Cap, 0603, +50V, ±5%, X7R | Various |
| R2 | 0.01 Ω | Res, 0603,0.1W | Various |
| D1 | 6.8V | Diode,SOT23,ESD | Various |

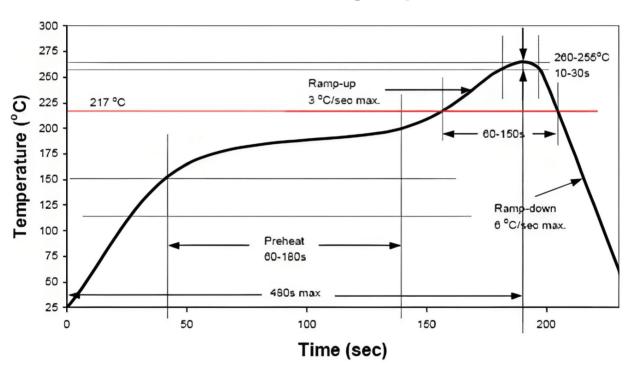


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Solderability

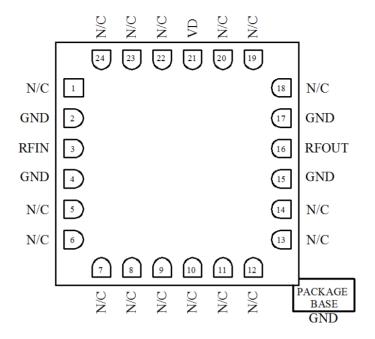
- 1. Compatible with lead-free soldering process with 260°C peak reflow temperature.
- 2. This package is non-hermetic, and therefore cannot be subjected to aqueous washing. The use of no-clean solder to avoid washing is highly recommended.

Recommended Soldering Temperature Profile





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Biasing and Operation

Turn ON procedure:

- 1. Connect GND to RF and dc ground.
- 2. Apply positive drain voltage VD and set to +5.0 V.
- 3. Apply RF signal.

Turn OFF procedure:

- 1. Turn off the RF signal.
- 2. Turn off the positive drain voltage VD.

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